PATENT COOPERATION TREATY

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INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

(Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

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4. This report contains indications relating to the following items:			
Box No. I Basis of the report	Ì		
Box No. II Priority			
Box No. III Non-establishment of opinion with rega	olishment of opinion with regard to novelty, inventive step and industrial applicability		
Box No. IV Lack of unity of invention			
Box No. V Reasoned statement under Article 35(2)	2) with regard to novelty, inventive step or industrial		
applicability; citations and explanations Box No. VI Certain documents cited	is supporting such statement		
Box No. VII Certain defects in the international appl	olication		
Box No. VIII Certain observations on the international	i i		
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Date of submission of the demand Date	e of completion of this report		
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Form PCT/IPEA/409 (cover sheet) (January 2004)

Bo	x No. I	B	asis of the report				
1.	. With regard to the language, this report is based on the international application in the language in which it was filed otherwise indicated under this item.						
	Ш	This rep	eport is based on a translation from the original language into the following language is the language of a translation furnished for the purposes of:	;,			
			international search (under Rules 12.3 and 23.1(b))				
			publication of the international application (under Rule 12.4)				
			international preliminary examination (under Rules 55.2 and/or 55.3)				
2.	jurnisi	With regard to the elements of the international application, this report is based on (replacement sheets which have be furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally file and are not annexed to this report):					
		the inte	ternational application as originally filed/furnished				
	\boxtimes	the des	scription:				
		pages		as originally filed/furnished			
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			ence listing and/or any related table(s) – see Supplemental Box Relating to Sequence				
3.		The am	nendments have resulted in the cancellation of:				
			the description, pages				
			the claims, Nos.				
			the drawings, sheets/figs				
			the sequence listing (specify):				
			any table(s) related to the sequence listing (specify):				
4.		This representation made, single 70.2(c))	eport has been established as if (some of) the amendments annexed to this report since they have been considered to go beyond the disclosure as filed, as indicated i)).	and listed below had not been in the Supplemental Box (Rule			
			the description, pages				
			the claims, Nos.				
			the drawings, sheets/figs				
			the sequence listing (specify):				
			any table(s) related to the sequence listing (specify):				
* ,	If item	4 applies	es, some or all of those sheets may be marked "superseded."				

YES

Box No. V		Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement				
1.	Statement					
	Novel	ty (N)	Claims Claims	1.0	Y1	

Inventive step (IS)

Claims

1-8

Industrial applicability (IA) Claims 1-8

Claims

2. Citations and explanations (Rule 70.7)

Reference is made to the following documents:

D1: US 5047728 D2: EP 0528413 D3: WO 9705695

The single general concept (the requirement of unity of invention of the independent claims) of the application is the teaching of solving the problem of providing a network having low resistance for direct current and high impedance for alternating current by using an active load arrangement comprising a voltage-controlled transistor with an inductance feedback.

D1 comprises an amplifier having an active load with an inductor. The inductor is connected between the source and the gate. Furthermore, the inductance is connected between the source and the output.

D2 describes a gain control circuit comprising a coil between the source of a field effect transistor (FET) and the output (figure 2). The transistor serves as an active load.

D3 discloses an amplifier circuit including a FET as an active load. The use of an inductance between the source and the gate/output is shown in embodiments (figure 6, page 9, lines 21-25).

The single general concept of the two independent claims 1 and 4 is known from each of the cited documents. Although the cited documents do not describe active-load devices per se, it is clear that generating an active load according to the

.../...

Supplemental Box

In case the space in any of the preceding boxes is not sufficient. Continuation of: Box $\,V\,$

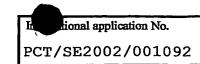
concept is known. Consequently, the concept is not inventive.

Claim 4 has an additional technical feature by stating a capacitance between the gate and the output. First, the feature is not part of the single general concept. Second, for a person skilled in the art it is known that (stray) capacitances are always present between connectors of components. Third, a capacitance connected between the gate and the output is known from for instance D2, figure 21.

Accordingly, the invention according to claims 1 and 4 does not fulfil the requirement of novelty.

The inventions claimed in the remaining claims 2-3 and 5-8 are considered to be general common knowledge to a person skilled in the art. Accordingly, the inventions according to claims 2-3 and 5-8 do not fulfil the requirement of novelty.

INTERNATIONAL PREDMINARY REPORT ON PATENTABILITY



Box No. VIII Certain observations on the international application

The following observations on the clarity of the claims, description, and drawings or on the question whether the claims are fully supported by the description, are made:

Claim 4 is vague in stating the purpose of the invention. "To provide a proper DC output" is considered being merely a statement of desiderata.

Form PCT/IPEA/409 (Box No. VIII) (January 2004)